Instructor: Dr. Virgilio Gonzalez, Clinical Associate Professor of Electrical & Computer Engineering  
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Office Hours: Monday 1 to 3pm, or by appointment  

Text: *Studying Engineering: A Road Map to a Rewarding Career, 4th Edition* by Raymond B. Landis  
*Introduction to Electric Circuits*, by Scott Starks (online)  

Purpose of Course: To enhance your success as a student of electrical and computer engineering and as a person  

Journal: Please purchase a notebook that will be used to document: 
1. All notes taken in class  
2. All course assignments  
3. Questions, issues, or perspectives you gained from the reading and would like to raise in class  
4. Your opinion of each section of the text  

Grading: The course will be based on the following: 
1. Attendance (10%)  
2. Grade on the Journal you keep (20%)  
3. Grade on class participation (10%)  
4. Grade on the two mid-term exams (15% each)  
5. Grade on term paper on “Design Your Process of Becoming a World-Class Engineering Student” (15%)  
6. Final Exam (15%)  

Attendance: As indicated, attendance is mandatory. Excused absences will be granted according to the rules promulgated in the University Catalog. Students with more than three absences will be dropped from the course.  

Class Participation: Based on the reading, you should develop at least three questions, issues, or perspectives you would like to raise in class. These should be documented in your Journal.  

Term Paper: You are required to write a final term-paper on "Design Your Process of Becoming a World-Class Engineering Student." Length is suggested to be about 700 words but  

Final Exam: The Final Exam will be held at (10:00 AM – 12:45 PM, Friday, May 16). Final Journals must be turned in no later than the time of the scheduled final exam  

YOU ARE REQUIRED TO BRING YOUR JOURNAL TO EVERY CLASS MEETING.