

Week #	Lecture #	Date	Topic
1	1	June 10, 2019	Course Information, Syllabus, Introduction to Digital Design, Basic Boolean operators, Basic Boolean Algebra, Equations and Logic gates
	2	June 11, 2019	Number Systems, Two's complement, Arithmetic operations
	3	June 12, 2019	Truth table, Logic Diagrams, Design of Circuits, Timing Diagrams
	4	June 13, 2019	Canonical Equations, Reduced Equations via Boolean Algebra
	5	June 14, 2019	K-maps and reduced equations, Design of Combinational System
2	6	June 17, 2019	Quine-McCluskey method of reduction
	7	June 18, 2019	<i>Exam #1</i>
	8	June 19, 2019	Analysis of Combinational Systems, reverse engineering, adder design, other gates, implementation guidelines
	9	June 20, 2019	MSI Device: MUXes, Decoders
	10	June 21, 2019	Introduction to Sequential Systems, Latches, Flip-Flop, timing diagrams
3	11	June 24, 2019	Registers, Multi-function registers, Counter Design
	12	June 25, 2019	Counter Design, Sequential machines
	13	June 26, 2019	Capturing behaviors with FSM
	14	June 27, 2019	<i>Exam #2</i>
	15	June 28, 2019	Moore and Mealy Machines. Design and Analysis.
4	16	July 1, 2019	Basic ASM Design
	17	July 2, 2019	ASM Design with MSI
	18	July 3, 2019	ASM Design with LSI
	19	<i>July 4, 2019</i>	<i>Independence Day – University closed, No Classes</i>
	20	Jul 5, 2019	Other ASM Design considerations
5	—	July 8, 2019	<i>Exam #3 (7:00pm to 9:45 pm.)</i>