

# COURSE SYLLABUS

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AR COURSE OFFERED:                    2016

SEMESTER COURSE OFFERED: Fall

DEPARTMENT:                    Computer Science

COURSE NUMBER:                CS3432

NAME OF COURSE:                Computer Architecture I

NAME OF INSTRUCTOR:            Dr. Eric Freudenthal

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**The information contained in this class syllabus is subject to change without notice. Students are expected to be aware of any additional course policies presented by the instructor during the course.**  
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## Learning Objectives

Learning Outcomes for CS3432, Computer Organization (Catalog Title: Architecture 1)

family	Prerequisite knowledge from CS1,CS2, digital design, discrete, and precalc	Students will be familiar with...	Students will be able to effectively apply skills...	Students will be able to analyze & synthesize solutions..
numeric representation & ops	- familiar with radixes, signed representations, and scientific notation		- convert hex, dec, signed-dec, binary binary metric - signed/unsigned comparison (flags,order) - Add-with-carry - cast/sign-extend - floating point	- can determine appropriate representations for elementary types and design low-level programs that compute arithmetic results

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linearization	<ul style="list-style-type: none"> <li>- algebra,</li> <li>- arithmetic &amp; control-flow structures of an oo language</li> <li>- block structures,</li> </ul>		<ul style="list-style-type: none"> <li>- expressions (incl side effects)</li> <li>- control flow (if/while/for)</li> <li>- translate boolean logic</li> <li>- branch tables</li> <li>- op on arrays, structs, and pointers</li> </ul>	<ul style="list-style-type: none"> <li>- can translate infix expressions and block-structured programming constructs to assembly language</li> </ul>
gross architecture	<ul style="list-style-type: none"> <li>- able to program in at least one oo language</li> <li>- familiar with combinational and sequential logic</li> </ul>	Can describe the fetch-execute cycle in the context of the roles of PC, SP, flags, registers and memory.	<ul style="list-style-type: none"> <li>- select appropriate instructions</li> <li>- specify operand order</li> <li>- encode and decode instructions</li> <li>- specify addressing mode</li> <li>- utilize interrupt mechanism</li> <li>- implement interrupt handlers</li> </ul>	<ul style="list-style-type: none"> <li>- can implement and debug simple imperative programs in assembly or machine language</li> </ul>
timing	<ul style="list-style-type: none"> <li>- algebra</li> <li>- synchronous logic</li> </ul>		<ul style="list-style-type: none"> <li>- determine cycles/instruction</li> <li>- determine which instructions repeat in a loop</li> </ul>	<ul style="list-style-type: none"> <li>- can compute the execution time of a simple loop</li> </ul>
subroutine linkage & separate compilation	<ul style="list-style-type: none"> <li>- in oo languages studied in CS1/2</li> </ul>		<ul style="list-style-type: none"> <li>- parameter passing</li> <li>- return value</li> <li>- allocation of auto vars</li> <li>- register usage</li> <li>- global/local symbols</li> </ul>	<ul style="list-style-type: none"> <li>- can write or call a method with local variables, parameters, and return value in assy lang</li> </ul>
variable allocation	<ul style="list-style-type: none"> <li>- in oo languages studied in CS1/2</li> </ul>		<ul style="list-style-type: none"> <li>Can define and use variables with various..</li> <li>- scope: visibility (file/method/program)</li> <li>- variable lifetime (program/method)</li> <li>- size</li> <li>- alignment</li> <li>- arrays</li> <li>- pointers</li> <li>- structs</li> </ul>	<ul style="list-style-type: none"> <li>Can appropriately allocate static and auto variables including arrays and pointers in assembly language</li> </ul>

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tools	<ul style="list-style-type: none"> <li>- ide</li> <li>- hierarchical filesystems</li> </ul>		<p>Can effectively employ in the composition and debugging of programs</p> <ul style="list-style-type: none"> <li>- editor</li> <li>- compiler</li> <li>- make</li> <li>- svn</li> <li>- bash</li> <li>- gdb</li> </ul>	Can compose and debug simple programs in a command-line environment
written communication	<ul style="list-style-type: none"> <li>- proficient in english</li> </ul>		<p>Can</p> <ul style="list-style-type: none"> <li>- interpret technical documentation on familiar topics</li> <li>- describe implementations that they design</li> <li>- recognize/use technical terminology</li> <li>- appropriate documentation for code</li> </ul>	Can appropriately document simple programs
mature programming	<ul style="list-style-type: none"> <li>- proficient in OO programming</li> <li>- appropriate comments</li> <li>- can modularize</li> <li>- appropriate symbol names</li> <li>- coding style</li> </ul>		<p>Can utilize in a program</p> <ul style="list-style-type: none"> <li>- appropriate comments</li> <li>- modularize</li> <li>- imperative programming</li> <li>- appropriate symbol names</li> <li>- coding styles</li> </ul>	Can appropriately modularize and document simple programs consisting of multiple files
advanced topics		<ul style="list-style-type: none"> <li>- pipelining</li> <li>- vectorization</li> <li>- predicated instructions</li> </ul>		Can identify when these topics are relevant to constructing an efficient solution.

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devices	gates, latches, (de)multiplexers, ALUs, switches, counters	- gross characteristics of memory & storage devices - counter-timer	can implement - simple programmed i/o - interrupt handlers	Can design programs that implement simple programmed i/o and interrupt handling - can determine the types of storage devices suitable for a variety of uses.
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Students are also expected to demonstrate two generic families of skills (which are also assessed).

- Clerical accuracy: clerical errors do not significantly interfere with students' ability to construct correct solutions.
- Completion: students are able to construct complete solutions in the allotted time.

**A weekly schedule of course topics is published on the course web site.**

## **Labs**

In addition to the “lecture” course section, students must also register for and attend a separate lab section. All lab assignments must be completed and submitted on time as specified by the course web site except in exceptional circumstances explicitly approved by the instructor.

## **Course Grading**

Mid-term and final course grades are equal to the fraction of course learning outcomes for which the student has demonstrated mastery via solutions to problems within assessment instruments (quizzes, and exams, and labs).

Full credit for a skill is assigned if mastery is demonstrated three of the last five times it is assessed within a quiz, exam, or lab assignment. Letter grades correspond to the fraction of skills (course learning outcomes) for which mastery is demonstrated using the conventional 100 point percentage scale:

A: > 90%; B: >80%, C: >70%, D: >60%

Mastery is assessed using the following scale:

- Insufficient proficiency: no credit  
*Solutions do not indicate mastery as indicated in learning outcomes.*

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- 0: no evidence of familiarity
- 1: familiar, but not proficient
- Sufficient proficiency: full credit  
*Solutions indicate mastery as indicated in learning outcomes.*
  - 2: proficient: Solutions indicate ability to apply skill in problem-solving.
  - 3: exceptional: Solutions indicate exceptional mastery.

Students are provided multiple opportunities to demonstrate most skills during the course, and most problems provide opportunities to demonstrate multiple skills.

95% “essential” skills: Some foundational skills are critical for success in this course. Such skills will be identified as “essential. A due date for developing 95% reliable proficiency at those skills.

## **Quiz/Exam Schedule:**

- Final exam: date and time are specified by the university.
- Quarterly tests: dates will be announced in class and indicated on course web site.
- Frequent Quizzes
  - Generally unannounced
  - At least one every two weeks.
  - Generally do not need to be “made up”

## **Required Reading**

- Absolutely required: Kerningham, Brian W & Ritchie, Dennis M. "The C Programming Language, Second edition," Prentice Hall, ISBN: 0-13-115817-1.
- The course web site contains an online text on assembly language programming for the MSP-430.

## **Recommended Reading**

- MSPGCC cross-tools manual (55 pages). Can be downloaded from the course web site..
- Recommended by students (and by no way required): Android app "Programmer Mental Math" by Joel Jurix.

## **STANDARDS OF CONDUCT:**

Students are expected to conduct themselves in a professional and courteous manner, as prescribed by the Standards of Conduct. Students may discuss programming exercises in a general way with other students, but the solutions must be done independently. Similarly, groups may discuss project assignments with other groups, but the solutions must reflect their own

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creative work. Graded work should be unmistakably your own, and (portions of) solutions developed by others must be clearly documented. Transcriptions and paraphrasing of others' solutions are strictly prohibited. Professors are required to report academic dishonesty and any other violation of the Standards of Conduct to the Dean of Students.

### **Disabilities and Special Circumstances:**

All students should have the opportunity to succeed. It is essential to determine appropriate accommodations when a disability or personal/life circumstance has the potential to significantly interfere with your ability to succeed in this course.

- Disabilities, other significant circumstances, and accommodations must be documented and by Center for Accommodations and Support Services ([www.utep.edu/cass](http://www.utep.edu/cass)).
- Students are encouraged to discuss course-specific needs and accommodations with the course and lab instructors.