

Textbook: Required

Semiconductor Devices: Physics and Technology, 3rd Edition, S. M. Sze and M. K. Lee, Wiley, 2012

Evaluation:

| | Value |
|--------------------|-------|
| Problems/Exercises | 20% |
| Quizzes | 20% |
| Exam I | 20% |
| Exam II | 20% |
| Final Exam | 20% |
| Total | 100% |

Grading and Policies:

A: 90% - 100% B: 80% - <90% C: 70% - <80% D: 60% - <70% F: 0 - <60%

Late course work will not be accepted.

No make-up work will be given.

Prerequisite: PHYS 3325 (or EE 4395 Applied Quantum Mechanics for EE's) and EE 3321 each with grade of "C" or better.

Syllabus Changes: Some of the content in the syllabus is subject to change for improvements or other factors. Any changes will be communicated.

Academic Dishonesty:

Incidents of academic dishonesty will be referred to the Director of Electrical Engineering and the Dean of Students.

<http://studentaffairs.utep.edu/Default.aspx?alias=studentaffairs.utep.edu/dos>

The descriptions and definitions of academic dishonesty can be found at:

<http://admin.utep.edu/hoop> Look under Student Affairs and then Chapter one, section 1.3.1.

If you have a disability and need classroom accommodations, please contact The Center for Accommodations and Support Services (CASS) at 747-5148, or by email to cass@utep.edu, or visit their office located in UTEP Union East, Room 106. For additional information, please visit the CASS website at www.sa.utep.edu/cass.